1.1.3 Percentage of participation of full time teachers in various bodies of the Universities/Autonomous Colleges/ Other Colleges, such as BoS and Academic Council during the last five years.			
TIMELINE: 2014-15 TO 2018-19			
DEPARTMENTS	**No. Of REPRESENTATIONS**	**No. Of Faculty IN LAST 5 YRS**	**DEPT-WISE PERC. REPRESENTATION (%)**
CIVIL	3	81	3.70
CSE	4	138	2.90
EEE	2	60	3.33
ECE	4	130	3.08
MECH	2	130	1.54
DBM	1	41	2.44
REPRESENTATION OVER LAST 5 yrs	16	580	16.99

SUMMARY

- The BOS representation or Academic council representation over past 5 years under 5- Engineering programs & BBM are summarised in above table
- The HOD of dept. represents BOS at OU and also some senior faculty members are representing Academic councils of autonomous colleges etc
- The official mails from BOS or other relevant authorities are herewith attached

Details of Attachment

Program Name	Year	Name of teachers participated	Name of the body in which full time teacher participated
BE (ECE)	2018-19	Dr. B Krishna Kumar	BOS, ECE, Osmania University,Hyderabad
BE (ECE)	2017-18	Dr. N H Shobha	BOS, ECE, Osmania University,Hyderabad
BE (ECE)	2016-17	Mr.I Srikanth	BOS, ECE, Osmania University,Hyderabad
BE (ECE)	2015-16	Dr. Prabhu G Benakop	Member, Acadamic Council & Planning, JNTU Hyderabad

2019-20

CBCS 7 & 8 Sem
AICTE 3 & 4 Sem

Gmail

SHOBHA N H <shobhanh7@gmail.com>

Minutes of the meeting of heads with chairperson held on 5 feb 2019

Hema Rallapalli <drhemarallapalli@gmail.com> Thu, Feb 7, 2019 at 9:13 PM
To: Arifuddin Sohel <arif.sohel@mjcollege.ac.in>, Srikanth ImmaReddy <siri.vlsi@gmail.com>, krishnakumar bhagyanagar <krishnavishnu2004@yahoo.co.in>, kesavary@yahoo.co.in, Abdul Nayeem <hod_ece@deccancollege.ac.in>, hod ece <hod_ece@mvsrec.edu.in>, Hema rallapalli <hemalatha.rallapalli@gmail.com>, Chandra Sekhar Paidimarry <sekharpaidimarry@gmail.com>, SHOBHA N H <shobhanh7@gmail.com>, Nazia Parveen <nazia@mjcollege.ac.in>, S Aruna <aruna.suravarapu@gmail.com>, Bhavani Pitchuka <bhavani.pitchuka@gmail.com>

Dear members,

I thank you all for very patiently participating in all the meetings and maintaining the quorum. Since it is for the first time that we have to work towards a predefined curriculum, we need to make elaborate discussions and step ahead. Please look into the attachments for the minutes and scheme of AICTE model and CBCS VII and VIII sem. Also email the syllabus for all the subjects of VII and VIII sem under CBCS as well as III and IV sem of AICTE model by 12th Feb. Mark to all. Please Let me know if there are any discrepancies.

With Best Regards

Dr. Hemalatha Rallapalli
Chairperson, BOS
Dept. of ECE, UCE, Osmania University
9849199894

3 attachments

Minutes of Heads meeting on 5 feb.docx
17K

Proposed scheme of 7 and 8 semester under CBCS 5 feb.docx
19K

proposed AICTE model curriculum scheme as on 5 feb.docx
40K

HEAD OF THE DEPARTMENT
DEPARTMENT OF ECE
METHODIST COLLEGE OF ENGG. & TECH.
ABIDS, HYDERABAD

Minutes of the Meeting of meeting 5 feb 2019

Discussions on Proposed AICTE model curriculum syllabusof III & IV semester, scheme of all semesters under AICTE model and CBCS VII & VIII semester.

Members Present:

1. Dr. Hemalatha Rallapalli, Chairperson BOS (University), Dept. of ECE, UCE, OU
2. V.Karunakar Reddy, MatrusriEngg. College
3. P.Sravani, MatrusriEngg. College
4. Dr. B.Krishan Kumar, Methodist
5. Prof. A. Gopala Sharma, Head, Dept. of ECE, Stanley
6. Y.Latha , Stanley
7. Prof. M.A. Nayeem, Head, Dept. of ECE, Deccan
8. MrsB.Bhavani, Dept. of ECE, MVSR
9. Dr. B. Sarala, Head, Dept. of ECE, MVSR
10. Dr Ariffuddinsohel, HEAD, Dept. of ECE, MJCET
11. Dr. Sohel, Islamia

Minutes:

1. Chairperson, BOS welcomed the members for the meeting and informed the members that there were no common instructions from the Dean, FOE regarding the new AICTE model curriculum, Mandatory subjects, Open Electives etc.There was only aletter communicated on behalf of Dean, FOE to conducta meeting for revision of syllabus.
2. This was the second meeting following one held on 22-01-2019 and the following conclusions were drawn.
    a) VII & VIII semester subject Scheme were finalized as enclosed,as per the current CBCS scheme.
    b) As per AICTE model curriculum, the structure & scheme for III, IV, V, VI, VII & VIII semester were finalized with a total of 160 credits and not deviating much under each sections such as BS, HS etc. The scheme is as enclosed.
    c) Syllabus of various subjects in III & IV semester discussed and delegated to different colleges to be made (not deviating much from AICTE model)

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d) It is taken care that the existing infrastructure in various colleges would be put to use and included in the curriculum and there would also not be much deviation from the AICTE model.
e) Electronic workshop lab will consist of Networks Theory experiments.
f) CN lab will include experiments of networking with Raspberry Pi, other than network simulator.
g) VLSI design will include Verilog programming.
h) Embedded C & VLSI lab will consist of
    i. Arduino & Node MCU experiments.
    ii. Experiments on Cadence tools
    iii. Signal Conditioning using sensors (EMI lab as per AICTE)

3. Decision to be taken regarding microwave lab whether to continue with the benches only or include simulation also.
4. If summer internship is made mandatory, then the project work-stage 1 will include two seminars- one on summer internship and one on project work problem definition and literature survey.
5. Finally, the chairperson thanked all members for their active participation.

Sd/-
Dr.Hemalatha Rallapalli
Chairperson – BOS in ECE

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* CBCS 2018-19 III YR

PRE BOS Meeting of OU Affiliated Engineering Colleges

Agenda: Preparation of scheme and syllabus of V and VI semester - CBCS. 21/2/2018.

Attended by:

1. Dr. Hemalatha R, BOS
2. Dr. M.A. Nayeem, Head, DCET
3. Dr. Gopal Sharma, Head, Stanley
4. Dr. B. Sarala, Head, MVSREC
5. Dr. Mohammed Arifuddin Sohel, Head, MJCET
6. Dr. Shobha, Head, Methodist
7. Ms. C. Udayini, Stanley
8. Ms. B. Bhabani, MVSREC
9. Mr. M. Naresh, Matrushri

Minutes:

1. After due deliberations, the following scheme of V and VI semester is proposed to be submitted to BOS meeting for approval. The responsibility of preparing the syllabus is with the college listed in brackets)

V semester

S. No	Course Code	Course Title	Scheme of Examination		L	T	P	Hrs/ Wk	Credits
			CIE	SEE					
1.		Linear ICs and Application(DCET)	30	70	3	1	0	4	3
2.		Analog Communication(Meth)	30	70	3	1	0	4	3
3.		Antennas and Wave Propagation (MJCET)	30	70	3	1	0	4	3
4.		Automatic Control Systems(MJCET)	30	70	3	1	0	4	3
5.		Computer Organization & Architecture (MJCET)	30	70	3	1	0	4	3
6.		Digital System Design with Veriilog HDL(MVSR)	30	70	3	1	0	4	3
Practicals									
7		Linear IC Applications lab (DCET)	25	50	0	0	2	2	1
8		VERILOG HDL Lab(MVSR)	25	50	0	0	2	2	1
9		Mini Project(MJCET)	50	-	0	0	2	2	2
Total			**280**	**520**	**18**	**6**	**6**	**30**	**22**

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VI Semester

S. No	Course Code	Course Title	Scheme of Examination		L	T	P	Hrs/ Wk	Credits
			CIE	SEE					
1.		Digital Communication(MVSR)	30	70	3	1	0	4	3
2.		Digital Signal Processing(DCET)	30	70	3	1	0	4	3
3.		Microprocessor and Microcontroller(Stanley)	30	70	3	1	0	4	3
4.		Managerial Economics & Accountancy (Chair, BOS)	30	70	4	0	0	4	3
5.		Professional Elective-I	30	70	4	0	0	4	3
6.		Open Elective-I	30	70	4	0	0	4	3
	Practicals								
7.		Communication Lab(MVSR)	25	50	0	0	2	2	1
8.		Systems and Signal Processing Lab(Stanley)	25	50	0	0	2	2	1
9		Microprocessor and Microcontroller Lab(Stanley	25	50	0	0	2	2	1
10.		Summer Internship*(MJCET)							
Total			**255**	**570**	**21**	**3**	**6**	30	**21**

*Students have to undergo summer internship of 4 Weeks duration at the end of semester VI and credits will be awarded after evaluation.

PE # Professional Elective-I:

PE 602 EC Digital Image Processing (Mehtodist)

PE 603 CS Operating Systems (Stanley)

PE 605 EC Electronic Measurement and Instrumentation (Matrushri)

PE 606 EC Digital TV Engineering (Matrushri)

OE # Open Elective-I:

OE 601 CE Disaster Management (Civil +Methodist)

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OE 601 ME Robotics (MECHANICAL + MJCET)

OE 602 CS OOP using Java (CSE + DCET)

OE 601 EE Reliability Engineering (EEE +Stanley)

*OE 601 EC Embedded Systems (Stanley)

*OE 602 EC Hardware Description Language (MJCET)

*** OE 601 EC and *OE 602 EC Electives are not offered to the students of Electronics & Communication Engineering Department.**

2. General guidelines for preparation of syllabus is that it should be in the standard format with Five Course objectives and five course outcomes for each theory and practical subject.

3. For Reference books, There should be a heading called Suggested Reading and three titles should be mentioned for theory and one/two for laboratory.

4. It was suggested that Summer internship to be modified from 6 weeks to 4 weeks as there is very less time after the main exam in the summer vacation(two weeks only), a detailed discussion is requested during the BOS meeting to make internship more practically implementable.

5. Dr. Hemalatha is requested to allot subject codes after discussion with Dean.

6. Dr. Arif Sohel is requested to prepare a Google document of the detailed syllabus which is accessible to all so that they can add comments for changes to be taken up in BoS meeting.

Minutes prepared by

Dr. Arif Sohel

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*CBCS 2016-17 1yr.

E-mail sekharpaidimarry@gmail.com Fax: 040-27095179

DEPT. OF ELECTRONICS & COMMUNICATION ENGINEERING
UNIVERSITY COLLEGE OF ENGINEERING (AUTONOMOUS)
OSMANIA UNIVERSITY: HYDERABAD – 500 007

Subject : **Minutes of Meeting of Board of Studies in ECE, Osmania University, Hyderabad.**
Ref No : **442/ECE/OU/BoS/2016 dated 6th June, 2016**
Date, Time & Venue : 9th June, 2016 at 2:30 pm at UCE ECE Dept., OU.

The following were present:

1. Dr. P. Chandra Sekhar, Chairman, BOS in ECE and Convener
2. Dr. B. Rajendra Naik, Head, ECE, OU
3. Dr. R. Hema Latha, Dept. of ECE, UCE, OU
4. Dr. P. Laxmi Narayana, Professor/Principal Scientist, NERTU, OU
5. Dr. Kaleem Fatima, Dept of ECE, MJCET
6. Dr. MA Nayeem, Dept of ECE, DCET
7. Dr. DR Jahagirdar, Scienitst, RCI, Hyderabad
8. Dr. Nukala Srinivasa Rao, Dept of ECE, Matrusri College of Engineering
9. Mr. I. Srikanth, Dept of ECE, Methodist
10. Mr. A. Gopala Sharma, Dept of ECE, SCETW
11. Mr. Nuli Namasivaya, Dept of ECE, MVSRCE
12. Mrs. B. Sarala, Dept of ECE, MVSRCE
13. Mrs. Y. Latha, Dept of ECE, SCETW
14. Mr. Md. Misbahuddin, Dept of ECE, UCE, OU

1.1 Welcome

The meeting started with the Chairman and Convener welcoming the members. He introduced all the members attended. He briefed the members that a series of meeting of the Head of the Departments of OU and its Affiliated Colleges from 18th March, 2016 onwards was organized to discuss and propose the scheme of instruction of III/IV and IV/IV BE(ECE). After thorough discussions scheme of III/IV and IV/IV BE (ECE) has been proposed and sub committees were constituted to propose the syllabus of III/IV BE (ECE).

1.2 *Scheme & Syllabi of B.E (ECE) III/IV*

Chairman proposed the Scheme and Syllabi of III/IV and IV/IV BE (ECE) and the members approved the same. Syllabus of III/IV BE (ECE) is discussed in detail and members suggested some modifications. The same were incorporated in the contents as given in Appendix-I.

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I semester:

i. Pulse circuits are included in Digital IC applications.

ii. Computer Organization and Architecture is moved from III year II semester to III year I semester in place of Microprocessor and Microcontroller.

iii. Digital System Design with VERILOG HDL and VERILOG HDL lab are introduced.

II Semester

i. Analog and Digital Communication labs are merged.

ii. Electronic Instrumentation is moved to IV year.

1.3 ***Scheme of B.E (ECE) IV/IV***

The Scheme of BE IV/IV year is discussed and is approved as given in Appendix-II.

I Semester

i. Two electives are introduced instead of one in existing scheme.

ii. Computer Networks is shifted to IV II semester.

iii. Mobile and Cellular Communication is made elective in IV II semester.

II Semester

i. Radar and Satellite Communication is made elective.

ii. In addition, two electives are offered.

1.4 ***Scheme of Instructions and Examinations for BE I and II semester (CBCS Pattern)***

As per OU norms, we are to adopt Choice Based Credit System (CBCS) from the academic year 2016-17 onwards. In view of this, the existing rules of autonomous college will be adopted. It is proposed:

A. To adopt semester-wise nomenclature to describe the academic years of UG program as described below:

OLD	NEW
I year I semester	I semester
I year II semester	II semester
II year I semester	III semester
II year II semester	IV semester
III year I semester	V semester
III year II semester	VI semester
IV year I semester	VII semester
IV year II semester	VIII semester

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B. Each theory course offers three credits and lab course offers one credit.

C. The Scheme of Instruction is split into: Continuous Internal Evaluation (CIE) and Semester End Examination (SEE). For theory subject, CIE carries 30 marks and SEE carries 70 marks. For lab course CIE carries 25 marks and SEE carries 50 marks.

D. The grades will be allotted based on the marks secured in university exam and sessionals as per the following criteria.

Academic Performance	Letter Grade	Grade Points
90% and above	S	10
80 % and above	A	09
70 % and above	B	08
60 % and above	C	07
50 % and above	D	06
40 % and above	E	05
Below 40 %	F	0

E. Out of 30 sessional marks for theory, 10 marks are allotted for Assignments/Tutorials/Quizzes etc, in the course. The rest of the marks are to be based on the Internal test, weightage for each test is 10 marks.

F. Two internal tests will be conducted in each semester. Each will carry 20 marks, out of which 6 marks for Part-A consisting of Objective and short answer questions and 14 marks for Part-B consisting of subjective questions. Average of tow tests plus marks obtained in assignments/tutorials/quizzes etc will be taken as sessional marks.

G. Out of 25 or 50 sessional marks for practical, 10 or 20 allotted for viva-voce exam/test and 15 or 30 marks are allotted for laboratory record and observation.

H. The question paper will be in two parts: Part – A is compulsory and covers the entire syllabus and carries 20 marks. Part-B carries 50 marks and covers all the units of the syllabus.

I. Out of 50 marks for Project Work-1, 25 marks to be awarded by the guide/supervisor and remaining 25 marks to be awarded by a subject expert committee constituted by the concerned HOD.

J. The evaluation of BE project for maximum of 100 marks will be done as follows:
   a. 40 marks are allotted for quality of the project work covering a) literature review b) innovation/originality c) methodology and d) relevance/practical application.
   b. 30 marks are allotted to Report writing/Documentation.
   c. 30 marks are allotted to a) quality and clarity of presentation of project work and b) based on the performance in terms of his/her viva-voce examination and overall subject knowledge.

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1.5 ***Syllabus of Courses (Theory and Practicals) to be offered during I and II semesters.***

A. I Semester courses are common to all branches.

B. In II Semester, one core subject and lab are introduced: Basic Circuit Analysis and Electronic Workshop lab. The scheme is enclosed as Appendix 3

1.6 Scheme and Instructions and Examination for III, IV, V, VI, VII and VIII semesters as per CBCS requirements (to be tentative with minor changes permitted subsequently)

The courses offered in the existing scheme are almost mapped to the new scheme.

A. In III Semester, Signal analysis and Transform Techniques, Switching Theory and Logic Design, Environmental Studies and Electrical Technology are introduced. Electromagnetic Theory is shifted to IV semester.

B. In IV semester, Pulse and Digital Circuits, Networks Lab and Elements of Mechanical Engineering are introduced.

C. In V semester, Microprocessor and Microcontroller, MPMC lab and Antenna and Wave Propagation are introduced.

D. In VI semester, VLSI and Verilog Lab, one Professional Elective and one Open Elective are introduced.

E. In VII semester, Electronic Instrumentation, Computer Organization and Architecture, One Professional Elective and one Open Elective are introduced.

F. In VIII semester, Computer Networks and Two professional electives are offered. The detailed scheme is enclosed herewith Append 4.

1.7 ME Courses.

The Chairman informed the members that in ME three specializations are offered: Digital Systems (MJCET and DCET), Embedded Systems and VLSI Design (MVSR) and Embedded Systems (Stanley). The core and electives courses are presented to the members. It is felt that no modifications are done at ME courses. Modifications are proposed to an elective course entitled "Scripting Languages for VLSI Design Automation", which are approved. This course can be offered to all three specializations.

1.8 And any other matter with the permission of the Chair.

A. Dr. DR Jahagirdar suggested to allot credits depending the depth of the subjects i.e., there should be at least one subject in each semester with 4 credits.

B. Dr. N. srinivasa Rao felt that the number of credits is slightly less as per AICTE norms. He requested the authorities to look in to this matter.

C. He also suggested having relaxation of some credits/subject as is done in JNTUH.

D. The committee authorizes the Chairman to send the panel of examiners for BE, ME and Ph.D. courses in ECE.

The Chairman thanked the members for their active participation.

Chairman,
BOS in ECE (Global), Osmania University

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by Andhra Pradesh Legislative Act No. 30 of 2008)
Kukatpally, Hyderabad – 500 085 – Telangana – INDIA.

Dr. Avula Damodaram
B.Tech.(C.S.E.), M.Tech.(C.S.), Ph.D.(C.S.)
Professor of CSE &
Director, Academic and Planning

A1/2557/XVIII SCAS/

Date: 04.03.2015

To,
Dr. G. B. Prabhu,
Principal,
Indur Institute of Engineering & Technology,
Siddipet, Medak Dist.

Sir,

Sub: JNT University Hyderabad – Academic & Planning – Revision of Academic Regulations - Meeting of the Committee constituted by the University to be held on 05.03.2015- Intimation.

Ref: Minutes of the XVIII Meeting of the Standing Committee of the Academic Senate of JNTUH, Hyderabad held on 23.02.2014

***

With reference to the Minutes of the XVIII Meeting of the Standing Committee of the Academic Senate, the University constituted a Committee to examine the JNTUH Academic Regulations of the UG and PG programmes in comparison with the regulations of other Technological Universities and submit a report.

In this connection, you are nominated as a member of the Committee which is scheduled to meet on 05.03.2015.

You are requested kindly to attend the meeting. TA and DA will be paid as per University norms.

Yours faithfully,

DIRECTOR

Encl:

Principal i/c